

(7) リヤ デフォッガ回路

1

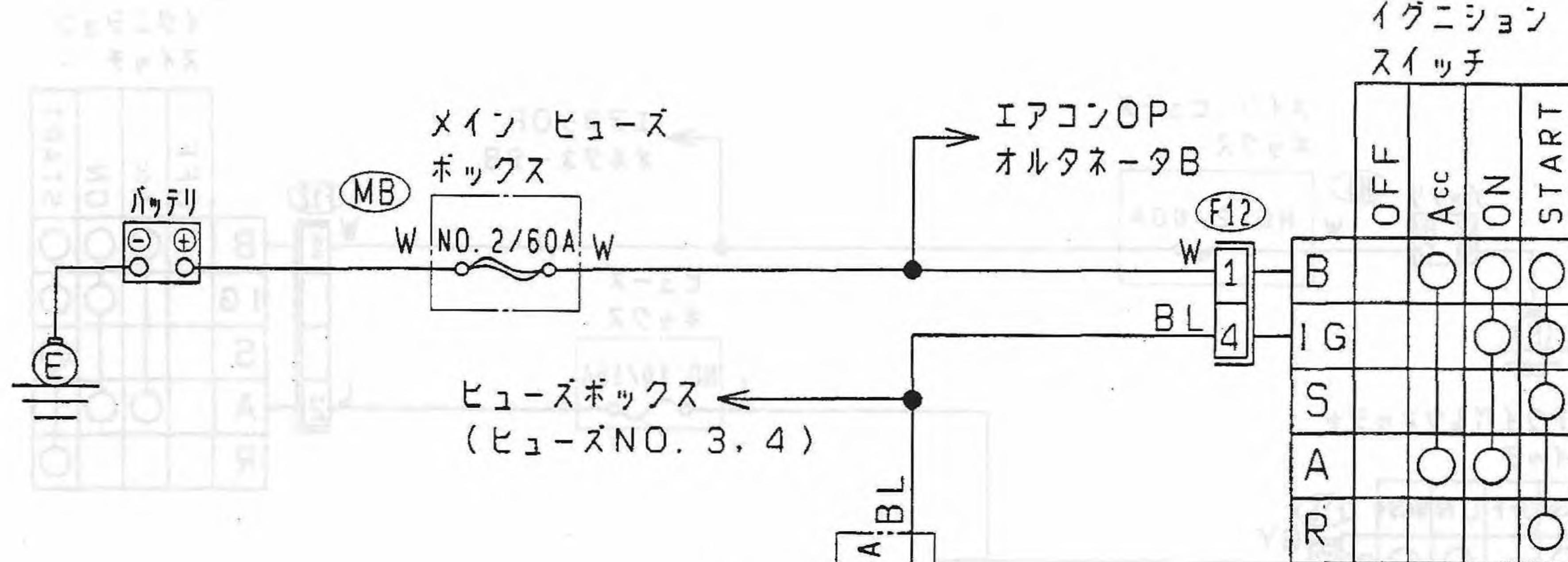
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3

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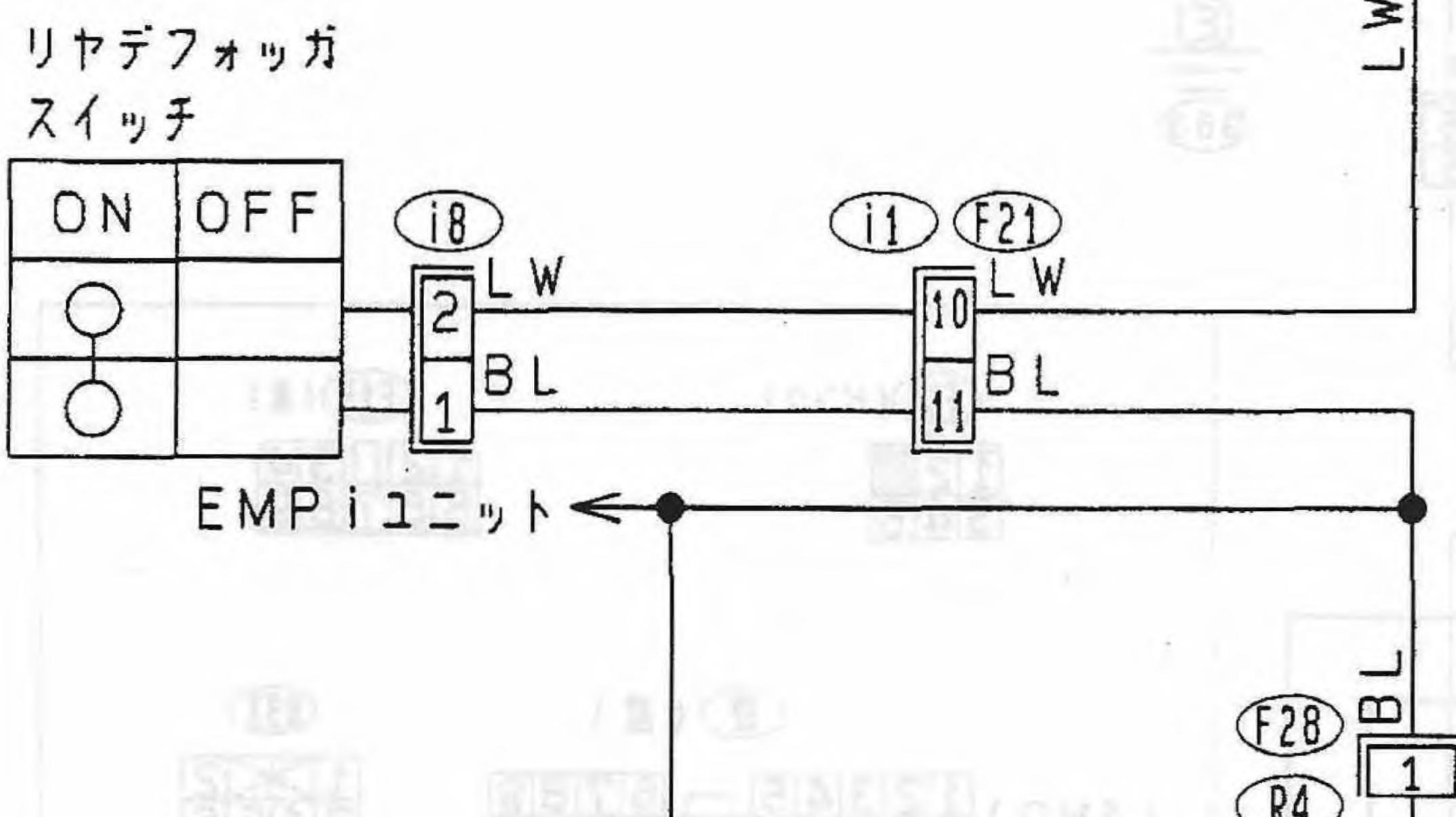
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A



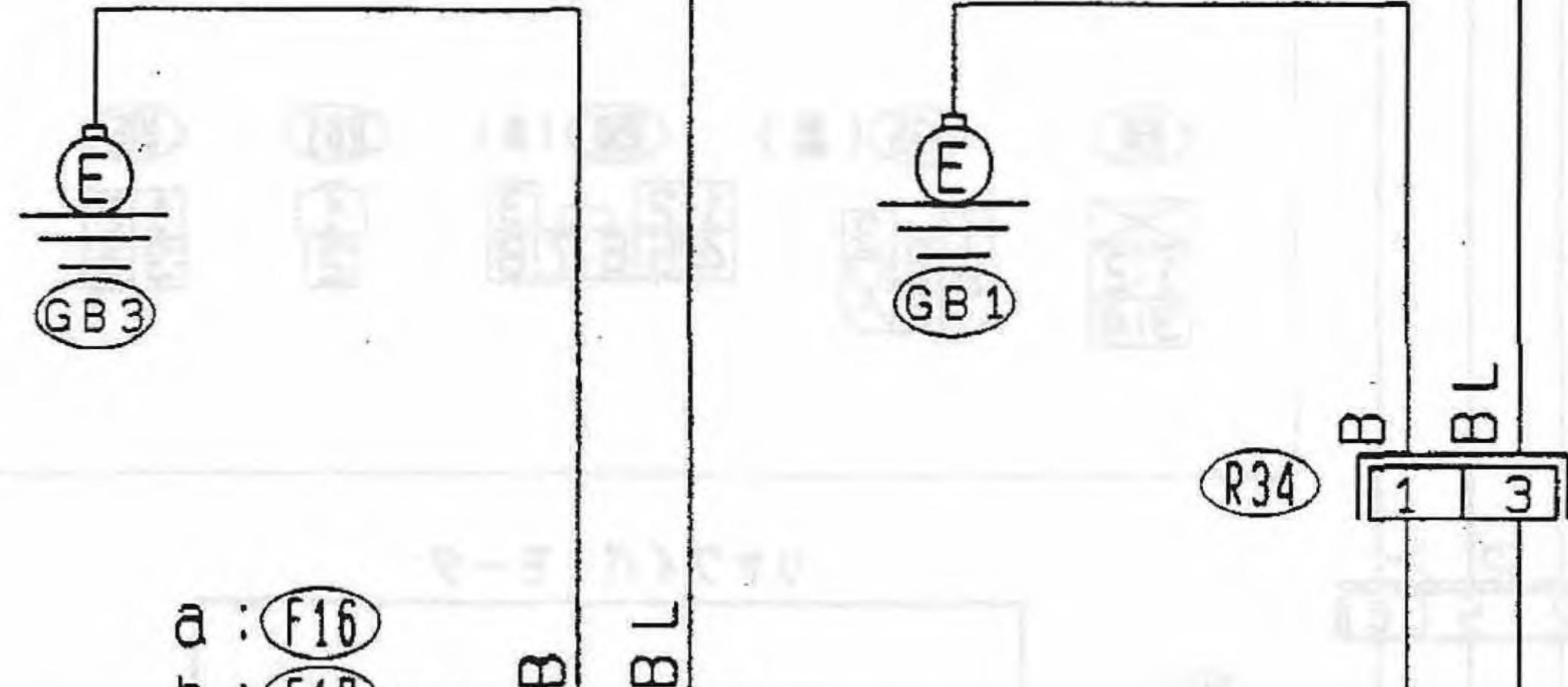
B

B



C

C



D

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